

US-120US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No: 10/686,974
Applicants: Rakesh Batish et al.
Filed: October 16, 2003
Title: SYSTEM AND METHOD FOR REDUCING OR ELIMINATING
SEMICONDUCTOR DEVICE WIRE SWEEP
TC/A.U.: 2823
Examiner: William D. Coleman

SUBMISSION OF FORMAL DRAWINGS


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Sir:

Submitted herewith are formal drawings for the above-identified application.

Respectfully submitted,

RatnerPrestia



Jacques L. Etkowicz, Reg. No. 41,738
Attorney for Applicants

JLE/kpc

Enclosures: (9) Sheets of Formal Drawings

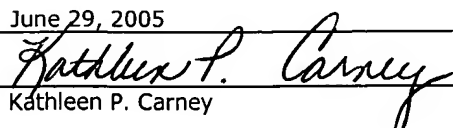
Dated: June 29, 2005

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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/686,974
	Filing Date	October 16, 2003
	First Named Inventor	Rakesh Batish
	Art Unit	2823
	Examiner Name	William D. Coleman
	Attorney Docket No.	K&S-120US
Total Number of Pages in This Submission 14		

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SIGNATURE OF APPLICANT, ATTORNEY OR AGENT			
Firm Name	RatnerPrestia		
Signature			
Printed Name	Jacques L. Etkowicz		
Date	June 29, 2005	Registration No.	41,738

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